

Floating impedance High Voltage Simultaneously Test Circuit And Test Method Thereof

Abstract

The invention relates to a floating impedance high voltage simultaneously test circuit, using two or more sets of conditions capable of simultaneously outputting different source signals to perform simultaneously the high voltage test and the low voltage DC impedance test, so that it is possible to simplify the test procedure of electric products, to reduce the poor yield of the facility, to reduce the test time, and to greatly increase the production efficiency; and to a method of performing the test.

(Fig.1)